

SNR-XFP-BIDI-CXX-10

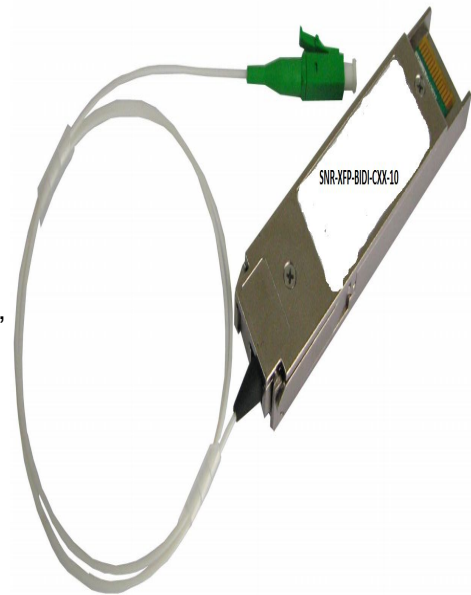
SNR XFP BIDI series

SNR-XFP-BIDI-CXX-10

CWDM Single-Mode Pigtail with LC/APC Connector for 10GbE/10FC
Bi-directional XFP Transceiver with same wavelength of TX/RX
RoHS 6 Compliant

Features

- ◆ Supports 9.95Gb/s to 11.1Gb/s data rates
- ◆ Power budget 9dB at least
- ◆ Tx/Rx Wavelength are compliant with ITU-T G.694.2, with 4 same wavelengths from 1270 to 1330
- ◆ LC/APC pigtail connector
- ◆ +3.3V power supply only
- ◆ Power dissipation <2W
- ◆ Built-in digital diagnostic functions
- ◆ Case temperature range:
Standard: 0~+70°C
- ◆ Complaint with XFP MSA
- ◆ Complaint with IEEE 802.3ae 10GBASE-LR/LW
- ◆ Complaint with 10GFC 1200-SM-LL-L



Applications

- ◆ 10GBASE-LR 10G Ethernet at 10.3125Gbps
- ◆ 10GBASE-LW 10G Ethernet at 9.953Gbps

Ordering information

Part No.	Data Rate	Laser	Temp.	Power Budget	Optical Interface	DDMI
SNR-XFP-BIDI-CXX-10	10Gbps	CWDM DFB	Standard	9dB	LC/APC/pigtail	YES

Note1: Standard version, X refer to CWDM Wavelength range 1270nm to 1330nm, A=1270nm, B=1290nm, C=1310nm,D=1330nm.

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CWDM Wavelength*note2

Band	Nomenclature	Wavelength(nm)		
		Min.	Typ.	Max.
O-band Original	A	1264	1270	1277.5
	B	1284	1290	1297.5
	C	1304	1310	1317.5
	D	1324	1330	1337.5

Note2: 4 Wavelengths from 1270 nm to 1330 nm, with 20 nm span. Please contact NAG to confirm the wavelength availability.

Regulatory Compliance

Product Certificate	Certificate Number	Applicable Standard
TUV	R50135086	EN 60950-1:2006+A11+A1+A12
		EN 60825-1:2007
		EN 60825-2:2004+A1+A2
UL	E317337	UL 60950-1
		CSA C22.2 No. 60950-1-07
EMC CE	AE 50135430 0001	EN 55022:2006
		EN 55024:1998+A1+A2
CB	JPTUV-024038-M1	IEC 60825-2
		IEC 60950-1
FCC	WTF13F0503735E	47 CFR PART 15 OCT., 2010
	WTF13F0503732E	47 CFR PART 15 OCT., 2010
FDA	1230816-000	CDRH 1040.10
ROHS	RLSZF00163462	2011/65/EU

Product Description

The SNR-XFP-BIDI-CXX-10 series single mode transceiver operates at a nominal wavelength of CWDM wavelength with same Tx/Rx waveform for Bi-direction optical data communications such as 10GBASE-LR/LW defined by IEEE 802.3ae and 10G Fiber Channel 1200-SM-LL-L. It is with the XFP 30-pin connector to allow hot plug capability.

This module provides digital diagnostic functions via a 2-wire serial interface as defined by the XFP MSA Rev 4.5.

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Absolute Maximum Ratings^{*note3}

Parameter	Symbol	Min	Max	Unit
Maximum Supply Voltage	V _{cc}	-0.5	4.0	V
Storage Temperature	T _s	-40	85	°C

*Note3: Exceeding any one of these values may destroy the device permanently.

Recommend operating condition

Parameter	Symbol	Min	Typ	Max	Units
Case Operating Temperature	T _c	0	-	70	°C
Power Supply Current	I _{cc}	-	-	580	mA
Supply Voltage	V _{cc}	3.13	-	3.45	V

Electrical Characteristics

Parameter	Symbol	Min	Typ	Max	Unit
Transmitter					
Data Rate		9.95	-	11.1	Gbps
Input differential impedance	R _{in}	90	100	110	Ω
Differential data input swing ^{*Note4}	V _{in,pp}	120	-	820	mV
Transmit Disable Voltage	V _D	2.0	-	V _{cc}	V
Transmit Enable Voltage	V _{EN}	GND	-	GND+ 0.8	V
Transmit Disable Assert Time		-	-	10	us
TX Disable	Disable	2.0		V _{cc} +0.3	V
	Enable	0		0.8	
TX Fault	Fault	2.0		V _{cc} +0.3	V
	Normal	0		0.8	
Receiver					
Differential data output swing ^{*Note4}	V _{out,pp}	340	650	850	mV
Output Differential Impedance	P _{IN}	90	100	110	Ω
Data output rise time ^{*Note5}	t _r	-	-	38	ps
Data output fall time ^{*Note5}	t _f	-	-	38	ps
LOS Fault	V _{LOS fault}	2.4	-	V _{cc}	V
LOS Normal	V _{LOS norm}	GND	-	GND+0.5	V

*Note4. Internal AC coupling.

*Note5. 20 – 80 %.

Optical Characteristics

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(CWDM DFB and PIN-TIA with 9dB Power Budget)

Parameter	Symbol	Min.	Typical	Max.	Unit
Power Budget		9			dB
Data Rate			9.953/10.3125		Gbps
Transmitter					
Centre Wavelength	λ	$\lambda_c - 6$	λ_c	$\lambda_c + 7.5$	nm
Spectral Width (-20dB)	$\Delta\lambda$			1	nm
Side Mode Suppression Ratio	SMSR	30			dB
Average Output Power ^{*note6}	$P_{out, AVG}$	-3		2	dBm
Extinction Ratio	ER	3.5			dB
Transmitter and Dispersion Penalty	TDP			2	dB
Average Power of OFF Transmitter				-30	dBm
Relative Intensity Noise	RIN			-128	dB/Hz
Relative Intensity Noise	RIN			-128	dB/Hz
Receiver					
Centre Wavelength	λ_c	1260		1340	nm
Sensitivity ^{*note7}	P_{IN}			-12	dBm
Receiver Overload	P_{MAX}	0.5			dBm
LOS De-Assert	LOS_D			-13	dBm
LOS Assert	LOS_A	-26			dBm
Rx_LOS	High	2.0		$V_{CC} + 0.3$	V
	Low	0		0.8	

*Note6. Output is coupled into a 9/125um SMF.

*Note7: Measured with a PRBS 2³¹-1 test pattern @10.3125Gbps.

Pin Descriptions

Pin	Logic	Symbol	Name/Description	Ref.
1		GND	Module Ground	1
2		VEE5	Optional -5.2 Power Supply – Not required	
3	LVTTL-I	Mod-Desel	Module De-select; When held low allows the module to , respond to 2-wire serial interface commands	
4	LVTTL-O	$\overline{\text{Interrupt}}$	Interrupt (bar); Indicates presence of an important condition which can be read over the serial 2-wire interface	2
5	LVTTL-I	TX_DIS	Transmitter Disable; Transmitter laser source turned off	
6		VCC5	+5 Power Supply, Not required	

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7		GND	Module Ground	1
8		VCC3	+3.3V Power Supply	
9		VCC3	+3.3V Power Supply	
10	LVTTTL-I	SCL	Serial 2-wire interface clock line	2
11	LVTTTL-I/O	SDA	Serial 2-wire interface data line	2
12	LVTTTL-O	Mod_Abs	Module Absent; Indicates module is not present. Grounded in the module.	2
13	LVTTTL-O	Mod_NR	Module Not Ready;	2
14	LVTTTL-O	RX_LOS	Receiver Loss of Signal indicator	2
15		GND	Module Ground	1
16		GND	Module Ground	1
17	CML-O	RD-	Receiver inverted data output	
18	CML-O	RD+	Receiver non-inverted data output	
19		GND	Module Ground	1
20		VCC2	+1.8V Power Supply – Not required	
21	LVTTTL-I	P_Down/RST	Power Down; When high, places the module in the low power stand-by mode and on the falling edge of P_Down initiates a module reset	
			Reset; The falling edge initiates a complete reset of the module including the 2-wire serial interface, equivalent to a power cycle.	
22		VCC2	+1.8V Power Supply – Not required	
23		GND	Module Ground	1
24	PECL-I	RefCLK+	Reference Clock non-inverted input, AC coupled on the host board – Not required	3
25	PECL-I	RefCLK-	Reference Clock inverted input, AC coupled on the host board – Not required	3
26		GND	Module Ground	1
27		GND	Module Ground	1
28	CML-I	TD-	Transmitter inverted data input	
29	CML-I	TD+	Transmitter non-inverted data input	
30		GND	Module Ground	1

Notes:

1. Module circuit ground is isolated from module chassis ground within the module.
2. Open collector; should be pulled up with 4.7k – 10k ohms on host board to a voltage between 3.15V and 3.6V.

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3. A Reference Clock input is not required.

Pin Arrangement

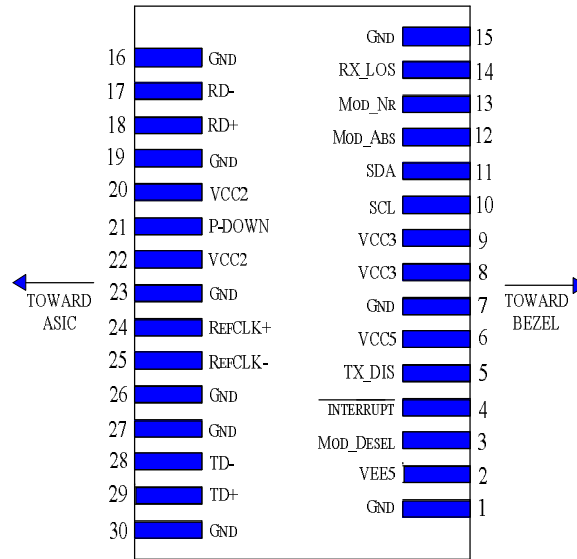


Diagram of Host Board Connector Block Pin Numbers and Name

Digital Diagnostic Functions

NAG SNR-XFP-BIDI-CXX-10 Small Form Factor 10Gb/s (XFP) transceivers are compliant with the current XFP Multi-Source Agreement (MSA) Specification Rev 4.5.

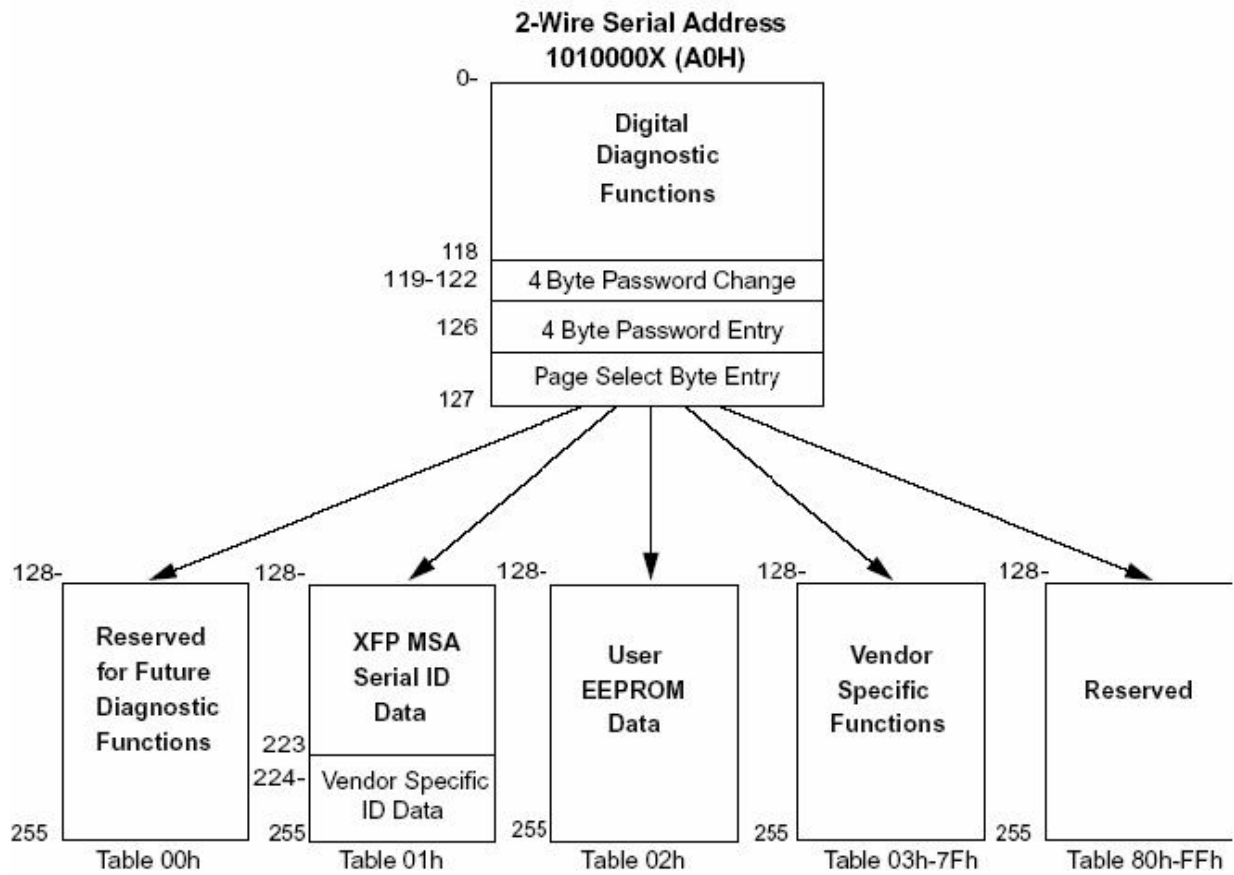
As defined by the XFP MSA, NAG XFP transceivers provide digital diagnostic functions via a 2-wire serial interface, which allows real-time access to the following operating parameters:

- ◆ Transceiver temperature
- ◆ Laser bias current
- ◆ Transmitted optical power
- ◆ Received optical power
- ◆ Transceiver supply voltage

The structure of the memory map is shown in the following figure, which is accessible over a 2-wire serial interface at the 8-bit address 1010000X (A0h). The normal 256 byte I²C address space is divided into low and upper blocks of 128 Bytes. The lower block of 128 Bytes is always directly available and is used for the diagnostics and control function. Multiple blocks of memories are available in the upper 128 Bytes of the address space. These are individually addressed through a table select Byte which the user enters into a location in the lower address space. Thus, there is a total available address space of 128*256 = 32 Kbytes in this upper memory space. The contents of Table 01h are listed in following table. Please refer SFF INF-8077i (Revision 4.5) for detailed information.

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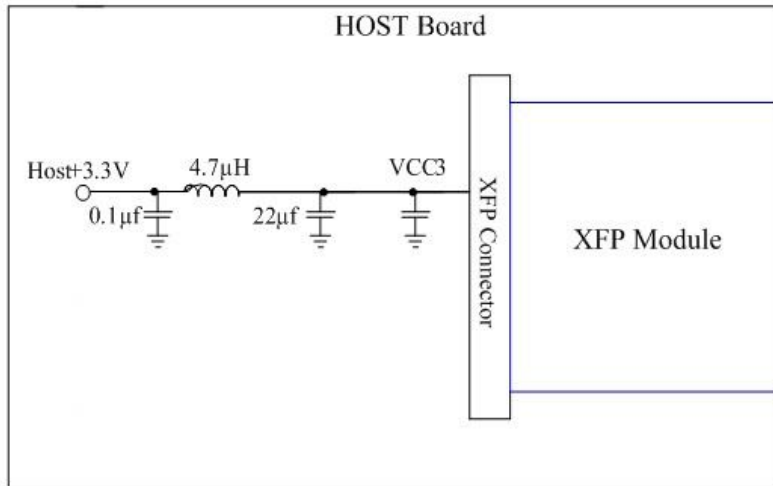
Monitor Specification:

Data Address	Parameter	Accuracy
96 ~ 97	Temperature	± 3°C
98 ~ 99	Reserved	
100~101	Tx Bias	±10%
102~103	Tx Power	±2dB
104~105	Rx Power	± 2dB
106~107	VCC3	± 3%

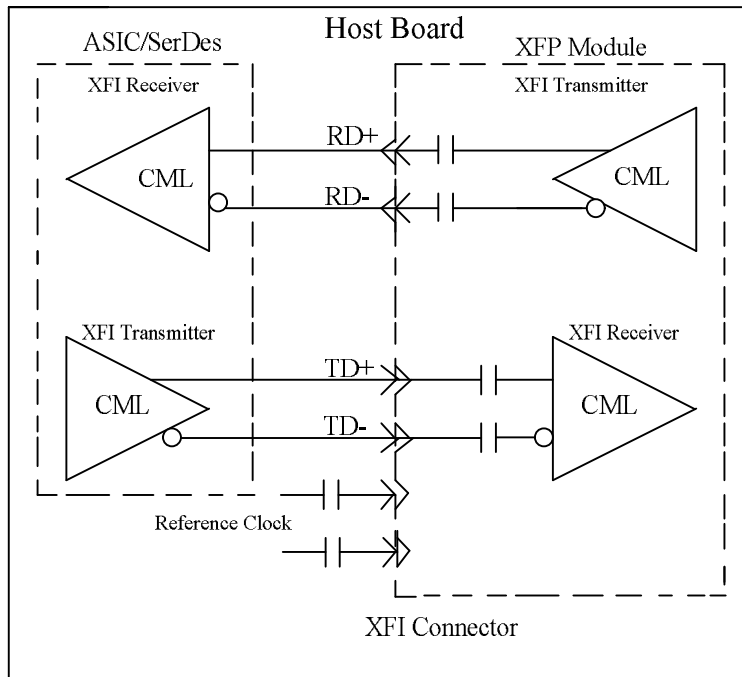
Recommended Host Board Power Supply Circuit

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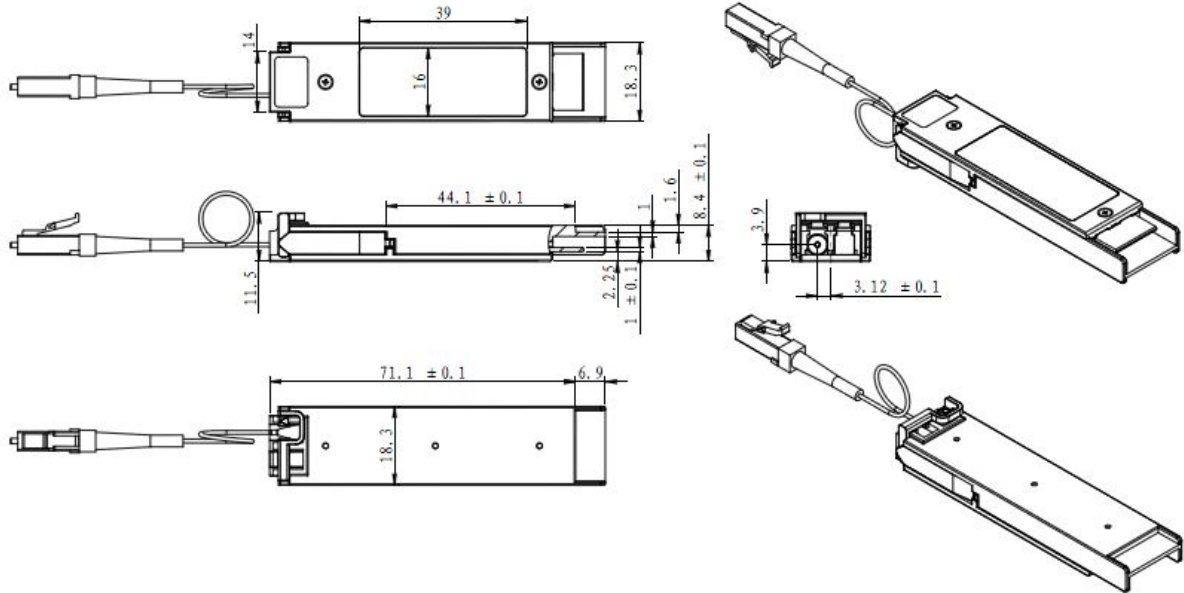
Recommended High-speed Interface Circuit



Mechanical Specifications

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NAG XFP transceivers are compliant with the dimensions defined by the XFP Multi-Sourcing Agreement (MSA).

Eye Safety

This single-mode transceiver is a Class 1 laser product. It complies with IEC-60825 and FDA 21 CFR 1040.10 and 1040.11. The transceiver must be operated within the specified temperature and voltage limits. The optical ports of the module shall be terminated with an optical connector or with a dust plug.

GUARANTEE:



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